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S/N 10/552301

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Torayuki TSUKADA	Examiner:	BAISA, J. S.
Serial No.:	10/552301	Group Art Unit:	2832
Filed:	October 7, 2005	Docket No.:	10921.0359USWO
Title:	CHIP RESISTOR AND METHOD FOR MANUFACTURING SAME		

CERTIFICATE UNDER 37 CFR 1.6(d)

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on May 12, 2008.

By: 

Name: Antonette Peters

REQUEST FOR CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Commissioner:

It is respectfully requested that this application be accorded the benefits of Continued Examination in accordance with the provisions of 37 C.F.R. §1.114.

A submission meeting of requirements of 37 C.F.R. 1.111 accompanies this paper.

Please charge the required filing fee of \$810 for a large entity to the Deposit Account No. 50-3478. Please charge any additional fee or credit any overpayment to Deposit Account No. 50-3478.

Respectfully submitted,

HAMRE, SCHUMANN, MUELLER &  
LARSON, P.C.  
P.O. Box 2902  
Minneapolis, MN 55402-0902  
(612) 455-3800

Dated: May 12, 2008By: 

Name: Douglas P. Mueller  
Reg. No. 30,300

DPM/acp

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